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RICHARD V. BURGUJIAN

571-203-2790

March 17, 2004

ATTORNEY DOCKET NO. 04329.3273
CUSTOMER NO. 22,852**Mail Stop Patent Application****Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

New U.S. Patent Application
Title: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD
FOR THE SAME
Inventor and Addressee: **Tetsuya YOKOI - Tokyo, Japan**

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application- 61 pages, including 2 independent claims and 19 claims total.
2. Drawings- 19 sheets of drawings containing 36 figures.
3. Certified copy of Japanese Patent Application No. 2004-006787, filed January 14, 2004.
4. Information Disclosure Statement and Information Disclosure Citation, PTO 1449 with documents attached.
5. The filing fee is calculated as follows:

Basic Application Filing Fee					\$770	\$ \$770.00
	Number of Claims		Basic	Extra Claims		
Total Claims	19	-	20		x \$18	
Independent Claims	2	-	3		x \$86	
<input type="checkbox"/> Presentation of Multiple Dep. Claim(s)					+\$290	
Subtotal						\$ 770.00
Reduction by 1/2 if small entity						-
TOTAL APPLICATION FILING FEE						\$ 770.00

A check for \$770.00 is enclosed.

This application is being filed under the provisions of 37 C.F.R. § 1.53(f).
Applicant awaits notification from the Patent and Trademark Office of the time set for filing the Declaration.

Applicant claims the right to priority based on Japanese Patent Application No. 2004-006787, filed January 14, 2004.

Please address all correspondence with respect to this application to:

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Please accord this application an application number and filing date.

The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or §1.17 during the pendency of this

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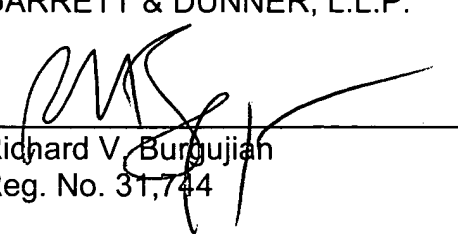
FINNEGAN
HENDERSON
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application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

By: _____


Richard V. Burgujian
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RVB/FPD/dvg
Enclosures